

Overview of Research Experience and Capabilities

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MHTL Research Capabilities



- Experimentation
- Modelling
- Numerical CFD analysis



Experimental Facilities



- Thermal interface material testing
- Air cooled heat sinks
 - ✓ thermal resistance and pressure drop
 - ✓ bypass
- Liquid cooled heat sink testing
- Thermal contact resistance for low contact pressures

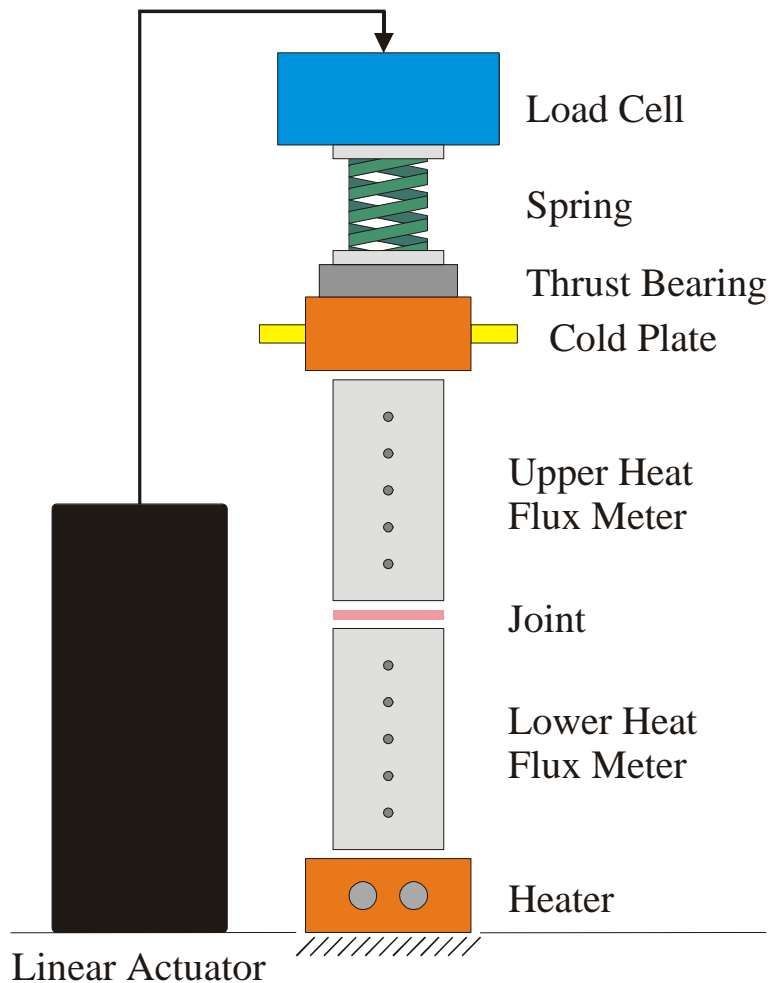


Thermal Interface Material Test



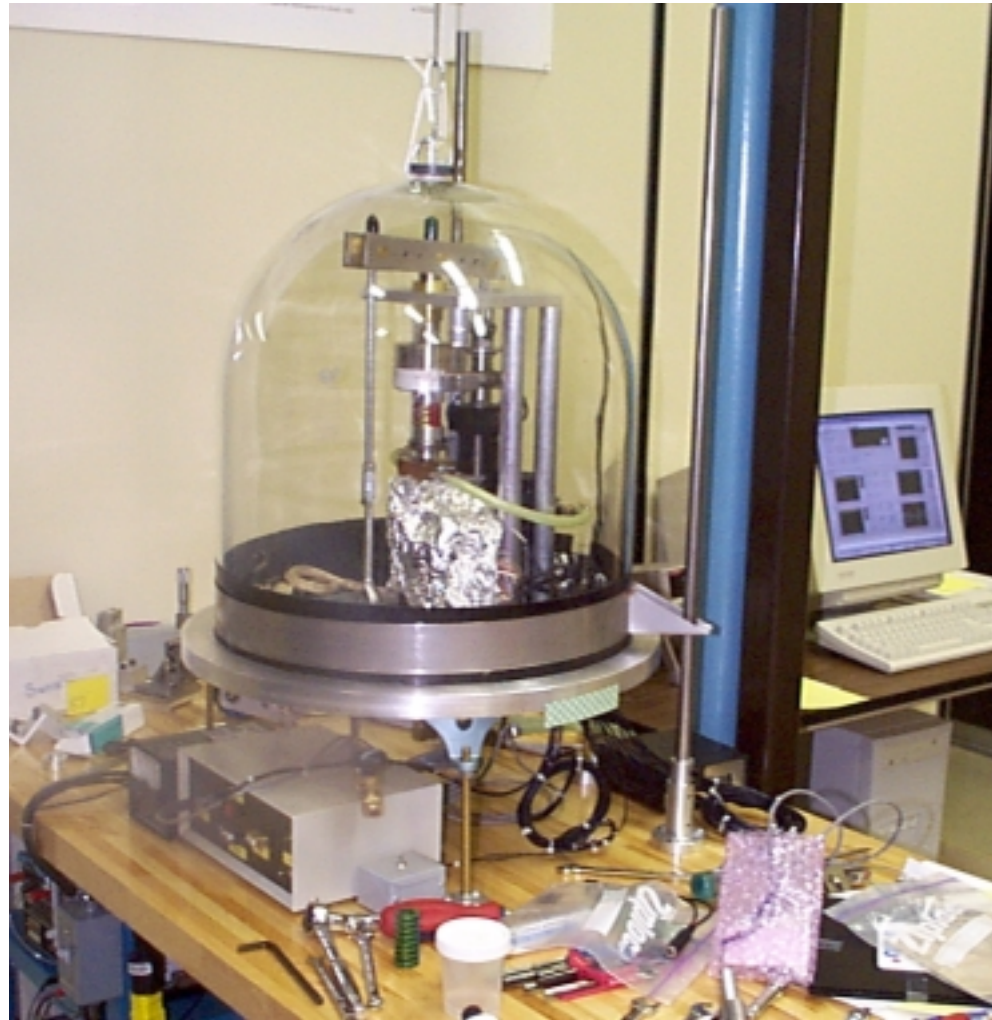
- Design, build & commission test apparatus & data acquisition interface for testing interface materials:
 - ✓ Measure joint resistance and thermal conductivity as function of:
 - interface temperature
 - contact pressure
 - material properties
 - surface characteristics
 - ✓ in-situ thickness measurement: sub micron precision

Apparatus



- Load cell
 - ✓ 100 or 1000 lbs
- Spring to compensate for thermal expansion
- Thrust bearing to remove torque loads
- Linear actuator
 - ✓ digitally controlled stepper motor
 - ✓ 400 steps / rev
 - ✓ 0.1 inch per revolution

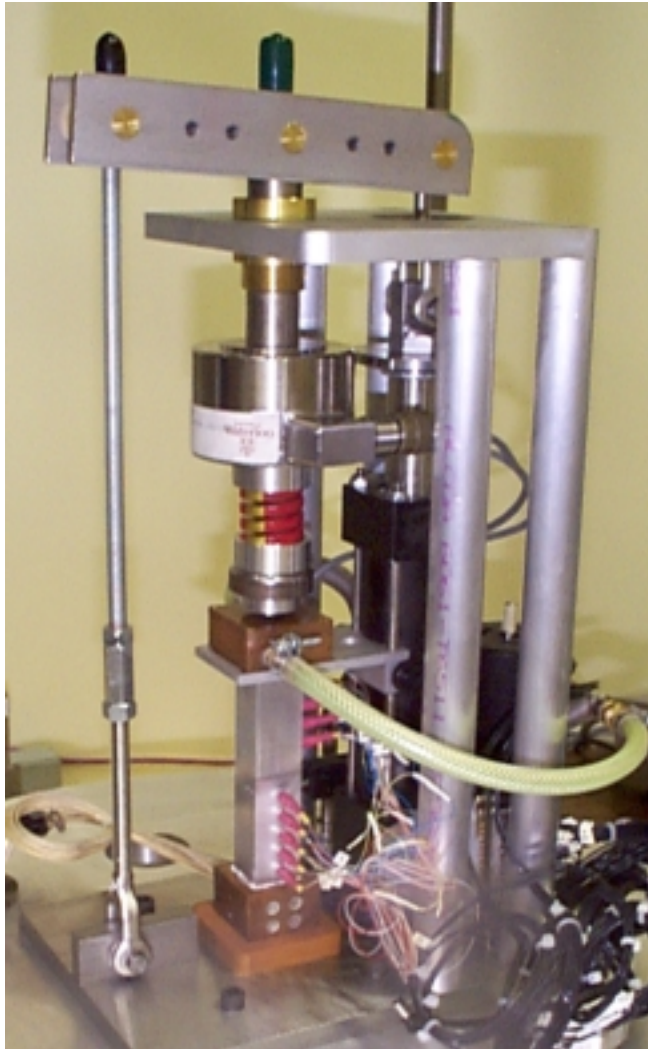
Thermal Interface Material Test



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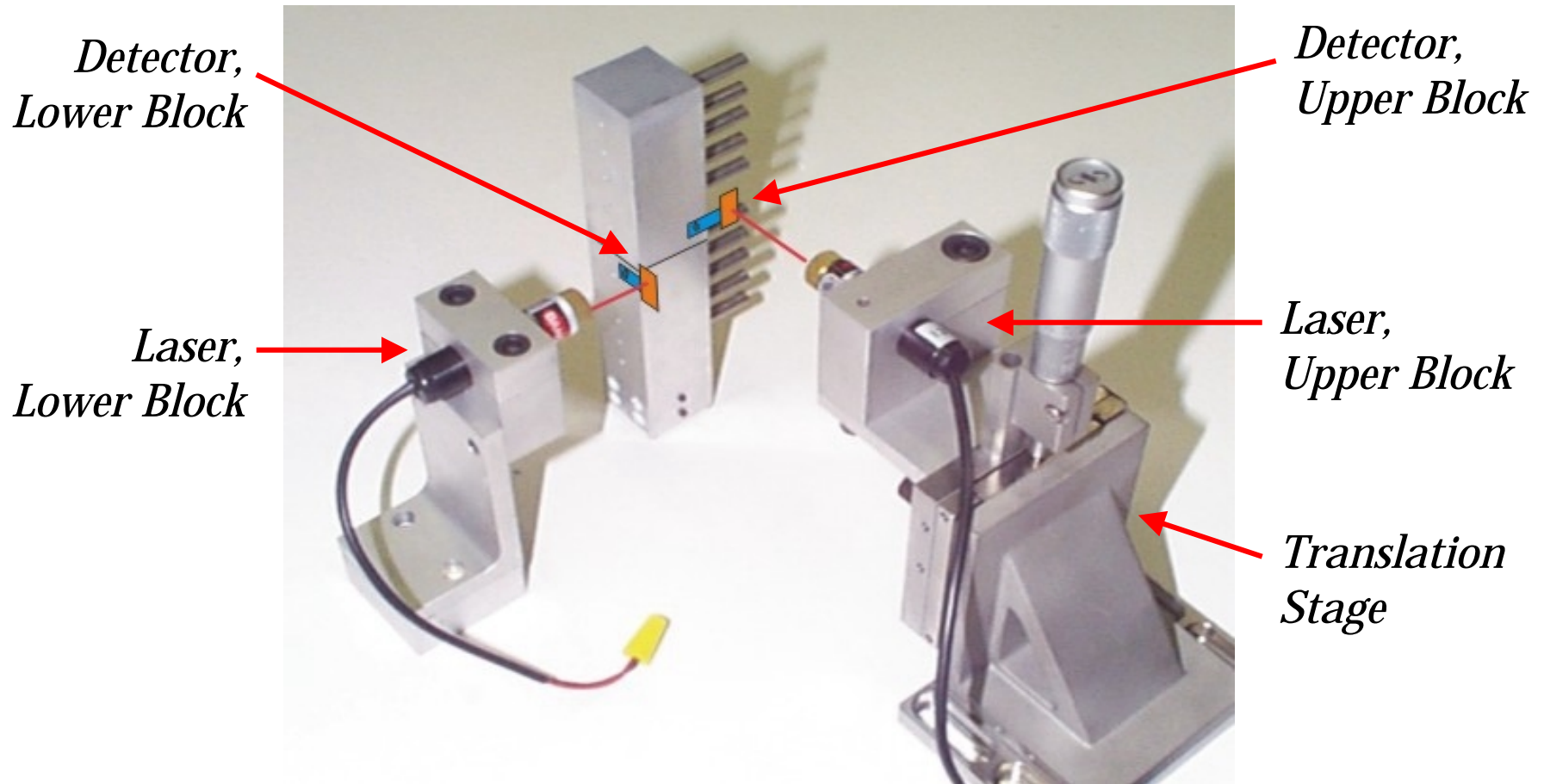
Thermal Interface Material Test



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Thickness and Deflection



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Air Cooled Heat Sink Tests



- Single-sided and back-to-back testing
- Wind tunnel
 - ✓ 18 inch x 18 inch x 24 inch tall section
 - ✓ 0 – 10 m/s
- Instrumentation
 - ✓ Keithley 2700 data acquisition system
 - ✓ 150 V, 7 A programmable DC power supply
 - ✓ Differential pressure transducers
 - ✓ Dantec hot wire anemometer
 - ✓ Pitot probe

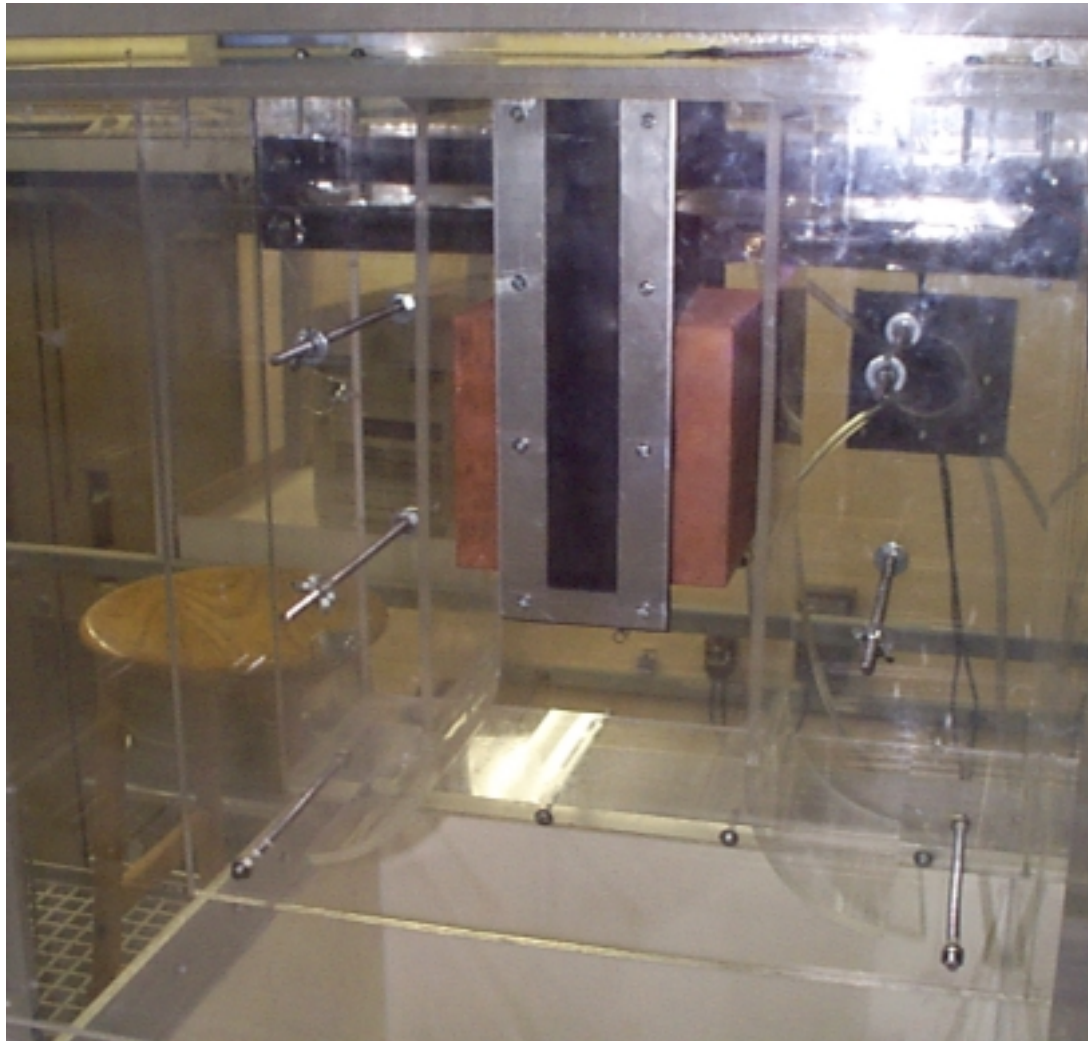
18" x 18" Open Circuit Wind Tunnel



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Heat Sink Bypass Measurement



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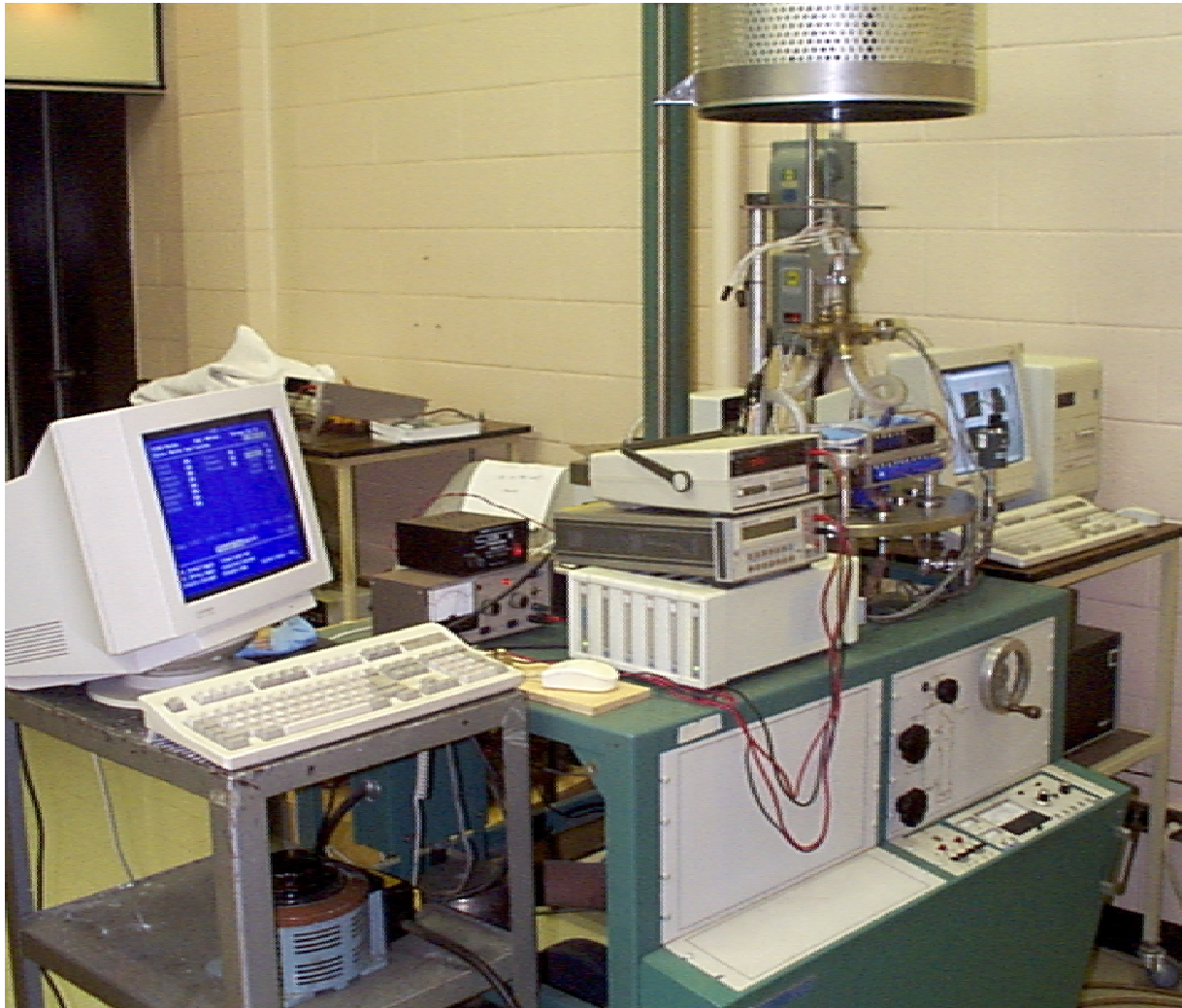


Liquid Cooled Heat Sinks



- High heat flux applications
- Vacuum environment to reduce losses
- Measurements:
 - ✓ power
 - ✓ temperature
 - ✓ flowrate
 - ✓ pressure drop
 - ✓ fluid temperature rise

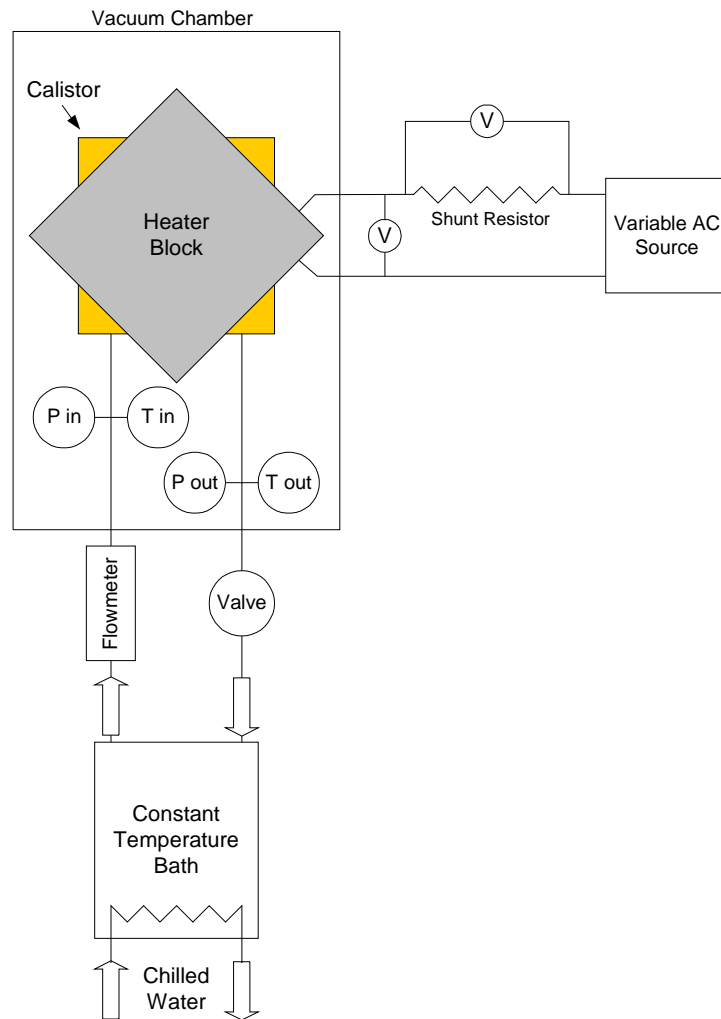
Liquid Cooled Heat Sinks



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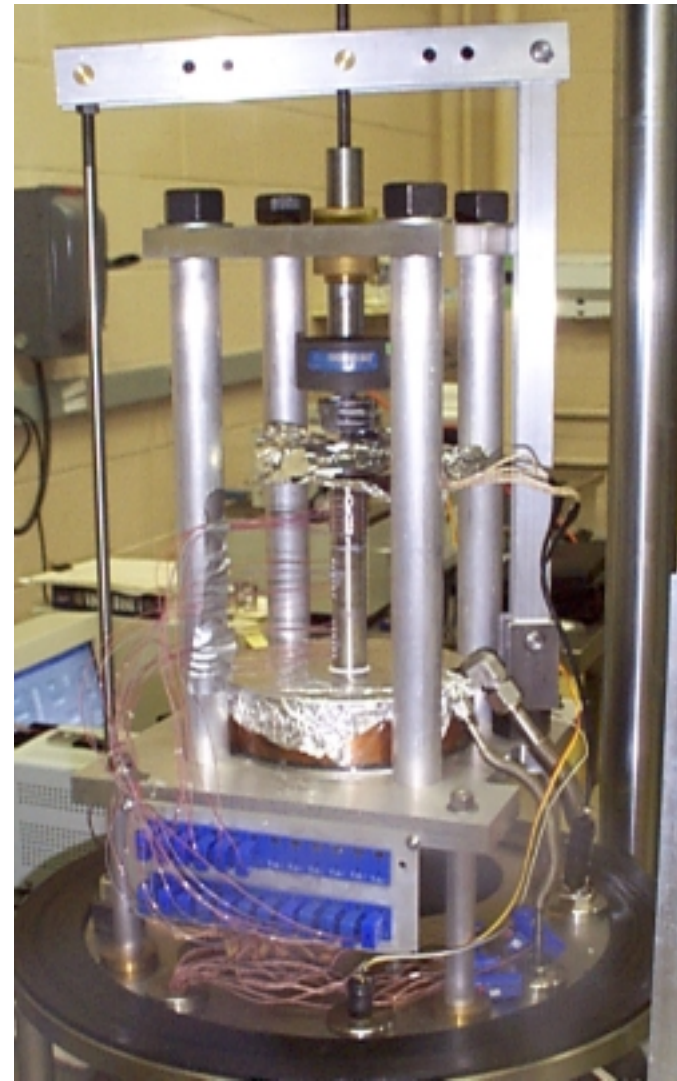
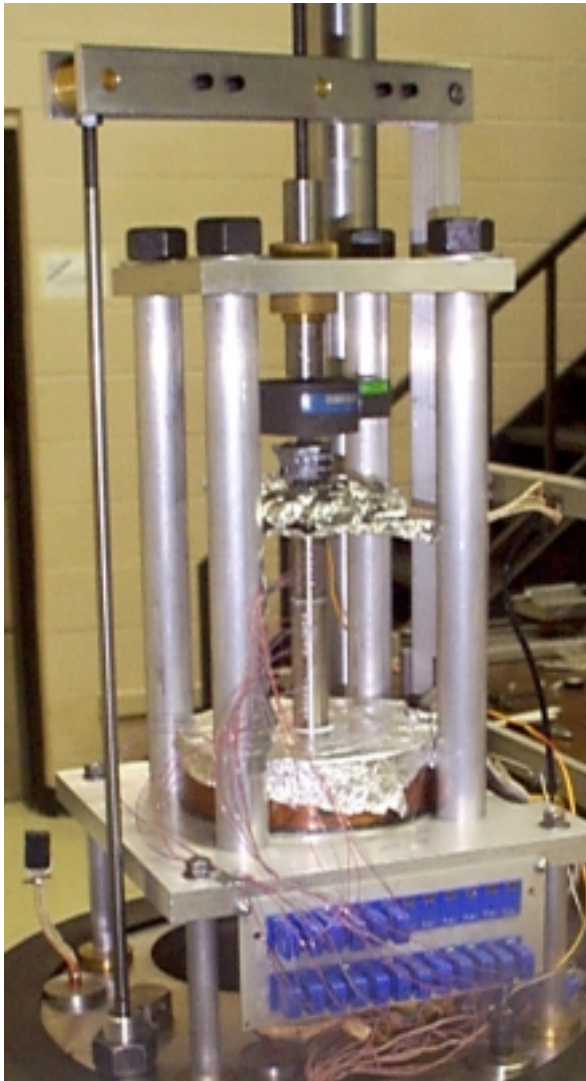
Liquid Cooled Heat Sinks



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Thermal Contact Resistance at Low Contact Pressure




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Model Development



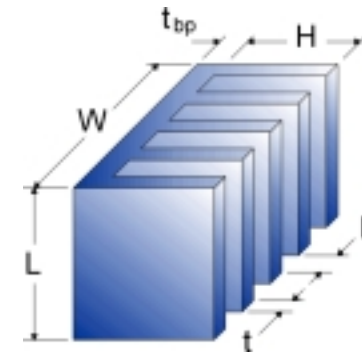
- Thermal model development:
 - chip level  cooling medium
 - ✓ heat sink optimization
 - ✓ modeling & characterization of thermal interfaces
 - ✓ modeling of spreading & constriction resistance
 - ✓ modeling of conduction & convection in PWBs
- Technology transfer:
 - ✓ Excel spreadsheets
 - ✓ Web-based analysis tools

Model Development



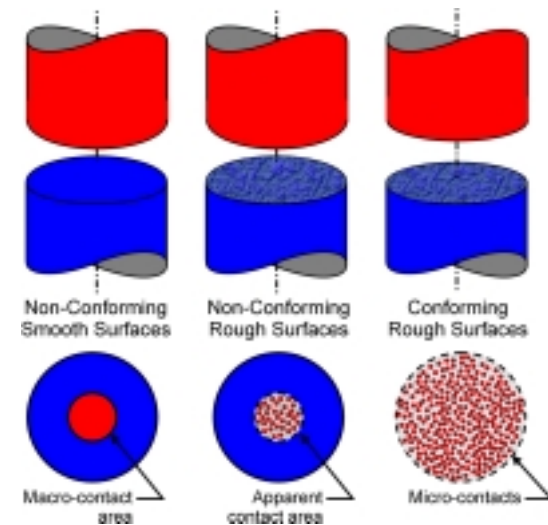
- Heat sink optimization model

- ✓ shrouded, air-cooled, plate fin heat sink
- ✓ interactive web-based modeling tool



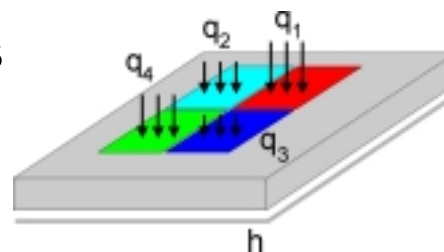
- Thermal resistance models

- ✓ non-conforming, smooth surfaces
- ✓ conforming rough surfaces
- ✓ Excel spreadsheet models



- Spreading resistance model for

- ✓ multiple discrete sources
- ✓ interactive web-based modeling tool



Heat Sink Optimization: Plate Fin - Microsoft Internet Explorer

File Edit View Favorites Tools Help

Back Search Favorites Media

Heat Sink Optimization: Plate Fin

Instructions Background Input/Output References

Optimize Value

Base Plate

Length mm

Width mm

Thickness mm

Fin

Height mm

Thickness mm

Number

Thermal Conductivity

Fin W/mK

Baseplate W/mK

Approach Velocity m/s

Maximum Dimensions

L mm

W mm

H mm

Calculate Reset

Typical Run Times				
Variables	1	2	3	4
Time (min)	1	3	10	30

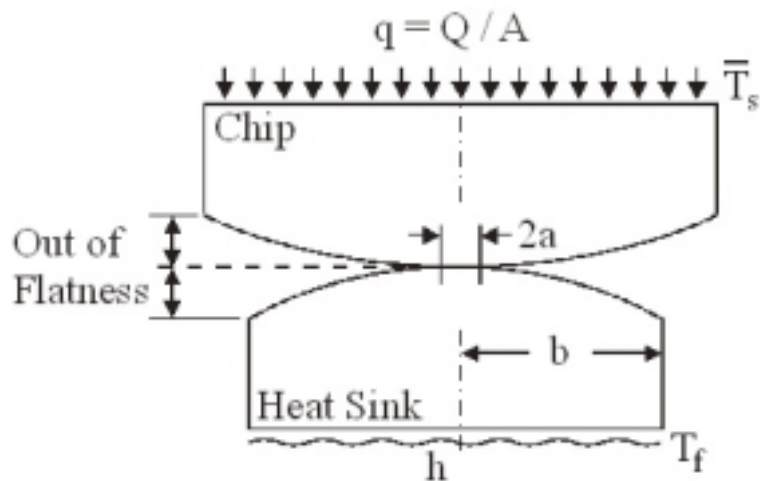
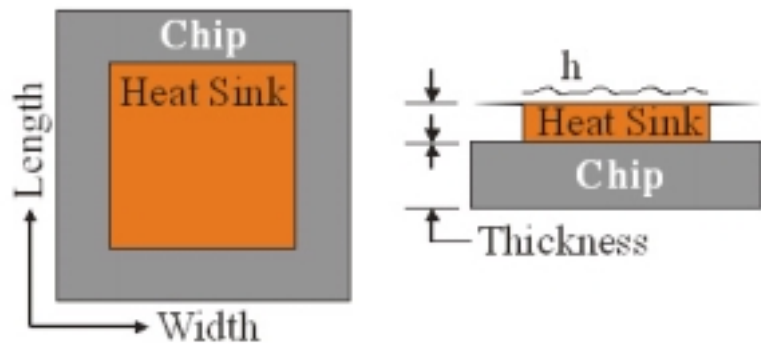
Value: provide specific values for constrained parameters

Optimize: indicate parameters to be optimized

Calculate: run optimization code to calculate design parameters for maximum thermal-fluid performance

Web URL: <http://mhtlab.uwaterloo.ca/onlinetools/optimize/index.html>

Contact Resistance for Non-Conforming Smooth Surfaces



$$R = \frac{\bar{T}_s - T_f}{Q} \quad h = \frac{Q_{\text{heat sink}}}{A_{\text{interface}} \Delta T_{\text{heat sink}}}$$

Chip and Heat Sink Geometry

	Chip	Heat Sink
Width (mm)	15	6
Length (mm)	15	6
Out of flatness (mm)	0.00762	0.00762
Thickness (mm)	0.75	0.5

Gap

Material	Air
P(atm)	1
T (°C)	50
k(W/m.K)	0.028
Beta [-]	1.643
Alpha [-]	2.44
Lambda (Micro m)	0.064

Chip and Heat Sink Material

	Chip	Heat Sink
Material	Silicon	Al 6063T5
k (W/m.K)	125	209
E (GPa)	163	70
Poisson ratio	0.30	0.30

Results

	Resistance (K/W)
Chip	
Heat Sink	
Gap	
Total	
Decimals	3

Condition

h (W/(m ² .K))	500
Contact Load (N)	7.41

Calculate

Exit

Help



Instructions: user's guide & sample problem

Background: governing eqns. & model development

Input/Output: data entry & units

References: publications & sample pdf files

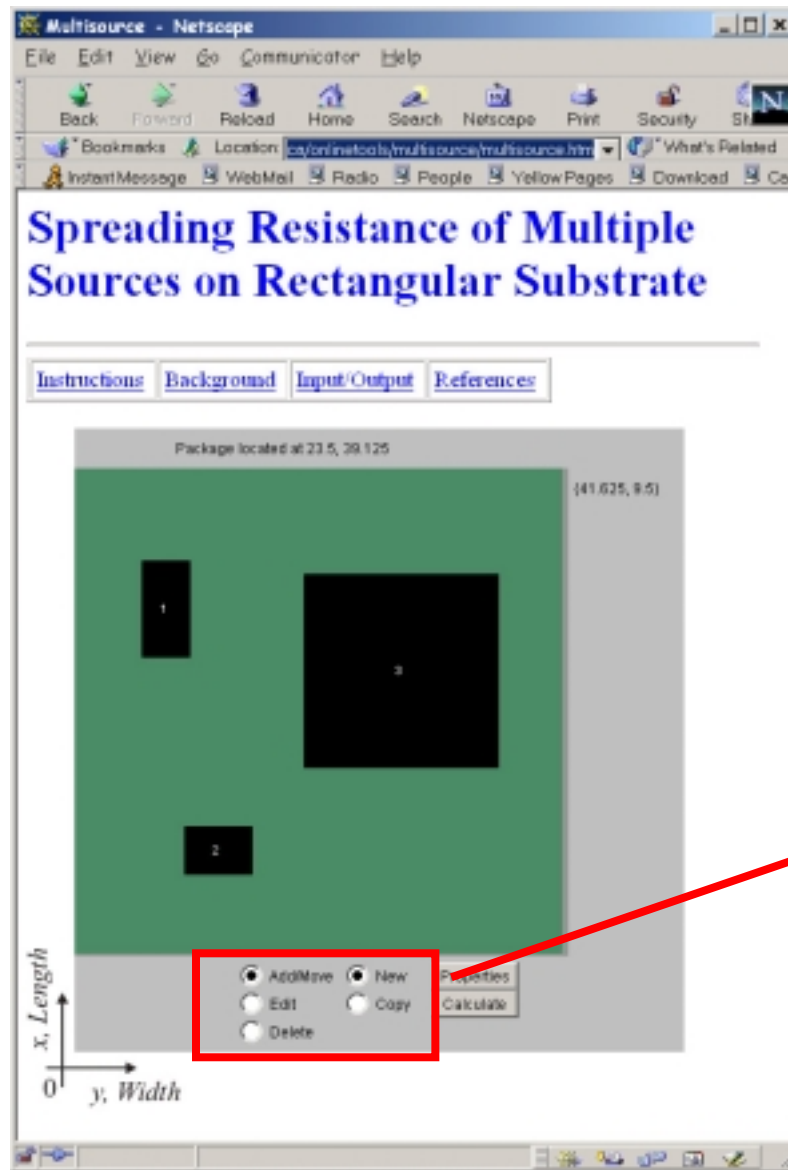
Properties: set substrate & source properties

Add/move, Edit, Delete,

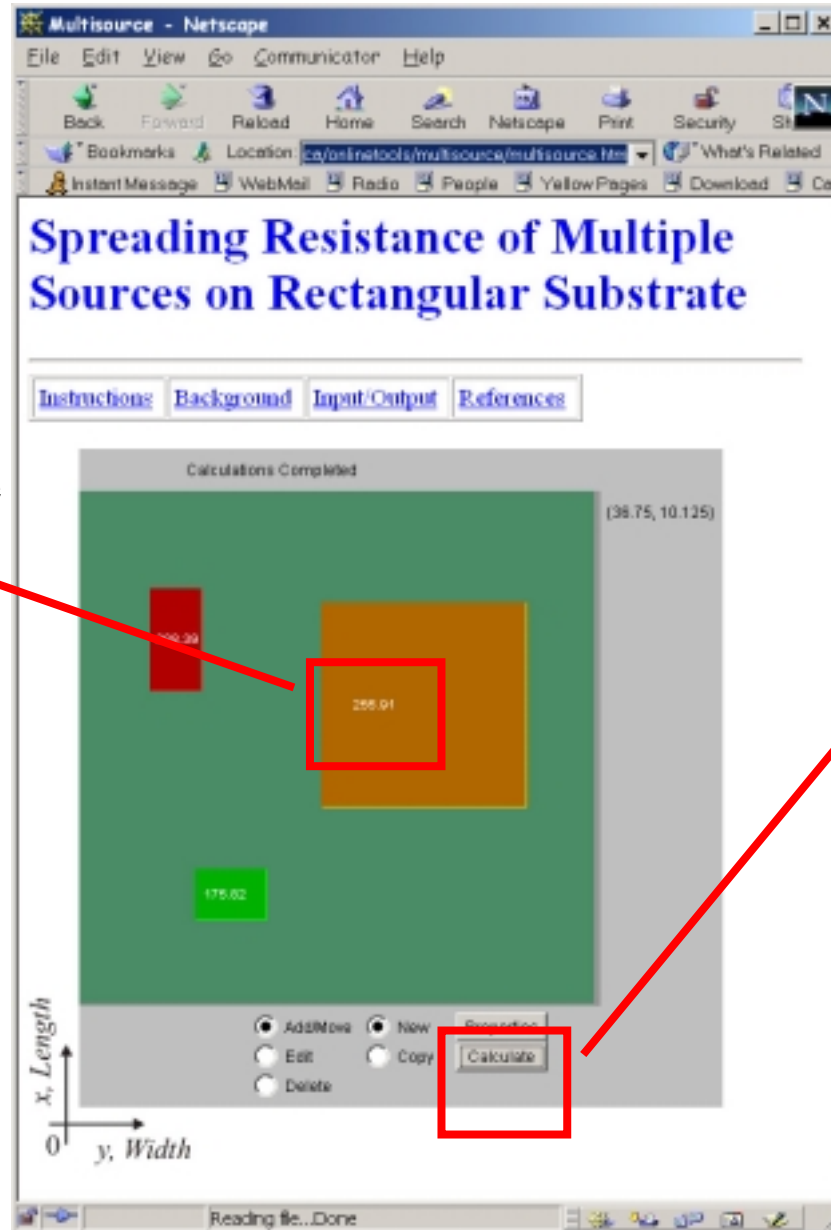
New, Copy: on screen package placement

Note: Java source requires Netscape (IE will not work)

Web URL: <http://mhtlab.uwaterloo.ca/onlinetools/multisource/index.html>



Add a new source: a pop-up window will appear for entering heat source inputs
- click on substrate to place current heat source



Mean source temperature rise - $^{\circ}\text{C}$

Calculate: click calculate to obtain mean heat source temperature rise for each source

- Java-based code will be executed on local CPU
- typical run times are approximately 10 seconds per source



Numerical CFD Analysis



- CFD modelling to support analysis:
 - ✓ parametric studies
 - ✓ validation
- Computing facilities
 - ✓ Sun Blade 1000 server
- Software
 - ✓ IcePak
 - ✓ Flotherm
 - ✓ I-DEAS